

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takayasu ITO et al.

Appln. No.:

Filed: HERewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants wish to make of record the documents cited  
in prior Application No. 09/874,001 filed June 6, 2001,  
whether cited by Applicants or by the Patent Office. The  
documents are listed on the attached Form PTO-1449.

Respectfully submitted,

NHS:jab

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October 27, 2003

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FORM PTO-1449

Atty. Docket No.

Appln. No.

XA-9483A

**LIST OF DOCUMENTS CITED BY APPLICANT**

Applicant

Takayasu ITO et al.

Filing Date

HEREWITH

Group

**U.S. PATENT DOCUMENTS**

Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA	4,931,665	6/5/90	Yee	327	538	
	AB	5,675,280	10/7/97	Nomura et al.	327	538	
	AC	6,424,128	7/23/02	Hiraki et al.	323	268	
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AL	9-289288	11/4/97	JAPAN			Abstract
	AM	2-224267	9/6/90	JAPAN			Yes
	AN						
	AO						
	AP						
	AQ						

**OTHER** (including author, title, date, pertinent pages, etc.)

	AR	
	AS	
	AT	

Examiner

Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.